

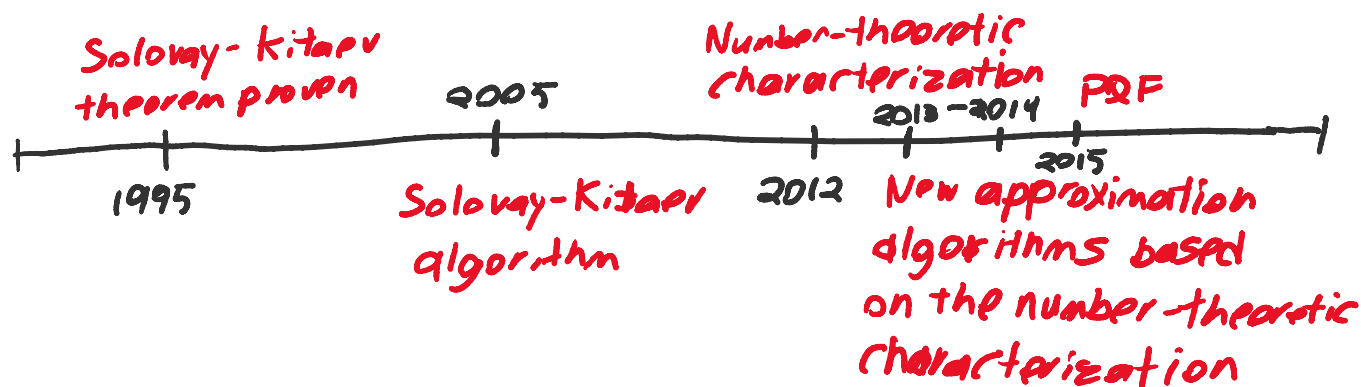
# Beyond Solovay-Kitaev

Since gate approximation produces **long** circuits with **many T gates**, optimizing single qubit gate approximations in Clifford+T is a fundamental problem in circuit compilation

For many years, the best-known **Constructive** method (Dawson & Nielsen 2005) gave  $C > 3$  for  $\{H, T\}$ . An information-theoretic lower bound of  $3 \log(1/\epsilon)$  suggested better was possible but wasn't met until the **Number theoretic method** was developed in 2013-2014. To this day, for  $\{H, T\}$  the best known (**pure circuit**) algorithm is Ross & Selinger (2014)'s **Grid-Synth** which produces approximations of length  $3 \log(1/\epsilon) + O(\log \log 1/\epsilon)$

If measurement is allowed, the **PQF** method of Bocharov, Roetteler & Svore (2015) reduces this to an expected length

$$\log(1/\epsilon) + O(\log \log 1/\epsilon)$$



# The Number-theoretic Method

The **Number-theoretic method** which has resulted in the proliferation of single-qubit approximation algorithms over Clifford+T, other gate sets, new (efficient) gate sets, and a deeper understanding of circuit groups, combines two parts:

- ① A <sup>constructive</sup> characterization of circuits over a gate set  $G$  as unitaries over an (algebraic number) ring  $R$
- ② Approximation of unitaries by "rounding off" into the ring  $R$

We will cover the first part in depth, and sketch a method for part 2.

## Thm. (Giles-Selinger)

Let  $\mathbb{Z}[i, 1/\sqrt{2}] = \{ \frac{1}{2^n} (a + ib + (c + id)\sqrt{2}) \mid n \in \mathbb{N}, a, b, c, d \in \mathbb{Z} \}$ .

A  $2^n \times 2^n$  unitary matrix  $U$  can be implemented as a Clifford+T circuit, possibly with ancillas, if and only if  $U \in M_{2^n \times 2^n}(\mathbb{Z}[i, 1/\sqrt{2}])$ . That is,  $U$  has all entries contained in  $\mathbb{Z}[i, 1/\sqrt{2}]$ .

### (historical context)

The Giles-Selinger theorem was first proven in the 1-qubit case and conjectured to hold for  $n$ -qubits by

Kliuchnikov, Maslov, & Mosca (2012, arXiv:1206.5236).

The **Giles-Selinger presentation** (2012, arXiv:1212.0506) is much cleaner however so we follow it.

# Algebra

## (Rings)

A ring  $R = (S, +, \cdot)$  is a set  $S$  equipped with binary operators  $+$ ,  $\cdot$  such that

$$\left. \begin{aligned} (a+b)+c &= a+(b+c) \\ a+b &= b+a \\ a+0 &= a \\ a+(-a) &= 0 \end{aligned} \right\} (S, +) \text{ is a group}$$

$$\left. \begin{aligned} (a \cdot b) \cdot c &= a \cdot (b \cdot c) \\ a \cdot 1 &= a = 1 \cdot a \end{aligned} \right\} (S, \cdot) \text{ is a monoid}$$

$$\left. \begin{aligned} a \cdot (b+c) &= ab+ac \\ (b+c) \cdot a &= ba+ca \end{aligned} \right\} \cdot \text{ distributes over } +$$

Ex.

The following are rings:

$\mathbb{Z}$  (the integers)

$\mathbb{R}$  (real numbers)

$\mathbb{Q}$  (rational numbers)

$\mathbb{Z}_2 = (\{0, 1\}, \oplus, \cdot)$  i.e. addition & mult. mod 2

$\mathbb{Z}_n = (\{0, \dots, n-1\}, a+b \bmod n, a \cdot b \bmod n)$

$\mathbb{D} = (\{a/2^b \mid a, b \in \mathbb{Z}\}, +, \cdot)$  ↖ regular add & mult

↖ Dyadic fractions like  $\frac{1}{2}, \frac{5}{1028}, 17, \frac{1}{64}$

## (Ring extensions)

Roughly speaking, for a ring  $R$  and  $\alpha \notin R$ ,  $R[\alpha]$  denotes the ring obtained by "adding  $\alpha$  to  $R$ ."

Ex. The following are ring extensions

$\mathbb{Z}[\frac{1}{2}] = \mathbb{D}$  (note that  $\frac{1}{2} \in \mathbb{Z}[\frac{1}{2}]$ , so  $\frac{1}{2} \cdot \frac{1}{2} \in \mathbb{Z}[\frac{1}{2}]$ )

$\mathbb{Z}[i, \frac{1}{\sqrt{2}}] = \mathbb{Z}[i][\frac{1}{\sqrt{2}}]$

$\mathbb{D}[w] = \{aw^3 + bw^2 + cw + d \mid w = e^{i\pi/4}, a, b, c, d \in \mathbb{D}\}$

# Unitaries over rings

## Fact

Clifford+T  
↓

Any circuit over  $\{H, CNOT, T\}$  corresponds to a unitary with entries in the ring  $\mathbb{Z}[i, \frac{1}{\sqrt{2}}]$ . In particular,

$$H = \frac{1}{\sqrt{2}} \begin{bmatrix} 1 & -1 \\ 1 & 1 \end{bmatrix}, \quad CNOT = \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 1 \end{bmatrix}, \quad T = \begin{bmatrix} 1 & 0 \\ 0 & w \end{bmatrix}$$

Note that  $w = e^{i\pi/4} = \frac{1+i}{\sqrt{2}}$ , so each gate is a unitary over  $\mathbb{Z}[i, \frac{1}{\sqrt{2}}]$ . Since  $\mathbb{Z}[i, \frac{1}{\sqrt{2}}]$  is a ring, multiplying two matrices over  $\mathbb{Z}[i, \frac{1}{\sqrt{2}}]$  produces a matrix over  $\mathbb{Z}[i, \frac{1}{\sqrt{2}}]$ .

## Note

Since  $w = \frac{1+i}{\sqrt{2}}$ ,  $w^* = w^{\dagger} = \frac{1-i}{\sqrt{2}}$ , hence  $\frac{w+w^*}{2} = \frac{1}{\sqrt{2}}$ .

So it follows that  $\mathbb{Z}[i, \frac{1}{\sqrt{2}}] = \mathbb{D}[w]$  which is more convenient to work with.

We have one direction of the proof with the above fact. To prove the other direction, we want a procedure (exact synthesis algorithm) that takes a unitary over  $\mathbb{D}[w]$  and produces a circuit over Clifford+T.

The basic idea is the same as CNOT + single qubit unitary synthesis: use two-level matrices to reduce one column  $v$  to  $\begin{bmatrix} 1 \\ 0 \\ \vdots \\ 0 \end{bmatrix}$  at a time



# Exact Synthesis

In CNOT + single qubit synthesis, we had access to **any** two-level matrix which could be used to map

$$\begin{bmatrix} u_1 \\ u_2 \end{bmatrix} \xrightarrow{U} \begin{bmatrix} 1 \\ 0 \end{bmatrix}$$

In Clifford + T, we only have 2 moves:

$$\begin{bmatrix} u_1 \\ u_2 \end{bmatrix} \xrightarrow{H} \frac{1}{\sqrt{2}} \begin{bmatrix} u_1 + u_2 \\ u_1 - u_2 \end{bmatrix}$$

$$\begin{bmatrix} u_1 \\ u_2 \end{bmatrix} \xrightarrow{T} \begin{bmatrix} u_1 \\ w u_2 \end{bmatrix}$$

The game is to alternate between **rotating** the phases of rows and then **adding rows of similar magnitudes** so that we can hopefully zero them out  
(think 2048...)

Ex.

Consider the column  $u = \begin{bmatrix} 1/\sqrt{2} \\ 1/2 \\ w/2 \\ 0 \end{bmatrix}$



To get  $u \rightarrow \begin{bmatrix} 1 \\ 0 \\ 0 \\ 0 \end{bmatrix}$  we intuitively need to sum the 2nd and 3rd rows first since they have the same magnitudes

However,  $1+w \neq 0$   
 $1-w \neq 0$

So we first need to rotate their relative phases.

$$\begin{bmatrix} 1/\sqrt{2} \\ 1/2 \\ w/2 \\ 0 \end{bmatrix} \xrightarrow{T_{2,3}} \begin{bmatrix} 1/\sqrt{2} \\ 1/2 \\ 1/2 \\ 0 \end{bmatrix} \xrightarrow{H_{2,3}} \begin{bmatrix} 1/\sqrt{2} \\ 1/\sqrt{2} \\ 0 \\ 0 \end{bmatrix} \xrightarrow{H_{1,2}} \begin{bmatrix} 1 \\ 0 \\ 0 \\ 0 \end{bmatrix}$$

We use powers of  $\frac{1}{\sqrt{2}}$  (the 1d) to guide pairing

# Denominator exponents

## Def'n

$\mathbb{Z}[\omega] = \{a\omega^3 + b\omega^2 + c\omega + d \mid a, b, c, d \in \mathbb{Z}\}$   
is the **ring of integers** of  $\mathbb{D}[\omega]$

## Ex.

The following are elements of  $\mathbb{Z}[\omega]$ :

$$\sqrt{2} = -\omega^3 + \omega$$

$$\omega^4 = -1$$

$$i = \omega^2$$

$$\omega^* = \omega^2 = -\omega^3$$

However,  $1/\sqrt{2} \notin \mathbb{Z}[\omega]$

## Fact

For any  $u \in \mathbb{D}[\omega]$ , there exists  $k$  s.t.

$$\sqrt{2}^k u \in \mathbb{Z}[\omega]$$

Explicitly, if  $q_{2^b} \in \mathbb{D}$ , then  $\sqrt{2}^k q_{2^b} \in \mathbb{Z}$   
for any  $k \geq 2b$

## Def'n (Ide)

— roughly, high Ide means small magnitude. Highest Ide's paired first

Let  $u \in \mathbb{D}[\omega]$ . The **least denominator exponent** of  $u$  is the **smallest**  $k$  s.t.  $\sqrt{2}^k u \in \mathbb{Z}[\omega]$ , denoted by **Ide**( $u$ )

## Ex.

$$\text{Ide}(1/\sqrt{2}) = 1 \text{ since } \sqrt{2} \cdot 1/\sqrt{2} = 1 \in \mathbb{Z}[\omega]$$

$$\text{Ide}(i/2 + \omega/\sqrt{2}) = 2 \text{ since } \sqrt{2}^2 \cdot (i/2 + \omega/\sqrt{2}) = i + \sqrt{2}\omega \in \mathbb{Z}[\omega]$$

↑ higher Ide / smaller magnitude  $= 1 + 2i$

# Denominator exponents of vectors

(Ide of a vector)

Let  $\vec{u}$  be a vector over  $\mathbb{D}[w]$ .  $\text{Ide}(\vec{u})$  is the smallest  $k$  s.t.  $\sqrt{2}^k \vec{u}$  is a vector over  $\mathbb{Z}[w]$

Ex.

$$\text{Ide}(\begin{bmatrix} 1 \\ 0 \end{bmatrix}) = 0$$

$$\text{Ide}(\tfrac{1}{\sqrt{2}} \begin{bmatrix} 1 \\ 1 \end{bmatrix}) = 1 \quad (\text{note: } \tfrac{1}{\sqrt{2}} \begin{bmatrix} 1 \\ 1 \end{bmatrix} = H \begin{bmatrix} 1 \\ 0 \end{bmatrix})$$

$$\text{Ide}\left(\begin{bmatrix} 1 \\ \frac{1}{\sqrt{2}} \\ \frac{1}{\sqrt{2}} \end{bmatrix}\right) = 1 \quad (\text{since } \sqrt{2} \in \mathbb{Z}[w])$$

$$\text{Ide}\left(\tfrac{1}{2} \begin{bmatrix} 1 \\ 0 \\ 0 \\ 1 \end{bmatrix}\right) = 2$$

# Single-qubit synthesis

Armed with the Ide, we can now tackle the problem of single qubit synthesis over  $\mathbb{D}[w]$ . Recall that a single-qubit unitary can be written as

$$U = \begin{bmatrix} a & -e^{i\theta} b^* \\ b & e^{i\theta} a^* \end{bmatrix}$$

We focus on the first column for now, i.e. finding some  $U_1 \cdots U_k \begin{bmatrix} a \\ b \end{bmatrix} = \begin{bmatrix} 1 \\ 0 \end{bmatrix}$

## Lemma (Ide-0-case)

Let  $u$  be a 2-dimensional unit vector over  $\mathbb{Z}[w]$ .

Then  $u = \begin{bmatrix} w^k \\ 0 \end{bmatrix}$  or  $\begin{bmatrix} 0 \\ w^k \end{bmatrix}$  for some  $k \geq 0$

Pf

The idea is to use the constraints imposed by  $\|u\|=1$

Note that if  $u = [u_1, u_2]^T$ ,

$$\|u\|^2 = u_1 u_1^* + u_2 u_2^* = |u_1|^2 + |u_2|^2$$

It can be shown that if  $u = aw^3 + bw^2 + cw + d$ , then

$$|u|^2 = a^2 + b^2 + c^2 + d^2 + (cd + bc + ab - da)\sqrt{2} \quad (\text{important})$$

Since  $\|u\|^2 = 1$ , the  $\sqrt{2}$  parts of  $|u_1|^2$  &  $|u_2|^2$  cancel. Now,

$a^2 + b^2 + c^2 + d^2 \in \mathbb{Z}^+$  when  $a, b, c, d \in \mathbb{Z}$ , so only 1 of  $u_1$  &  $u_2$  can be non-zero - i.e.  $u = \begin{bmatrix} v \\ 0 \end{bmatrix}$  or  $\begin{bmatrix} 0 \\ v \end{bmatrix}$ ,  $|v|^2 = 1$

Now,  $|v|^2 = 1 \Rightarrow a^2 + b^2 + c^2 + d^2 = 1 \Rightarrow$  only one entry is non-zero, and in particular  $v = w^k$ ,  $0 \leq k \leq 7$ .

Note: if  $u = \begin{bmatrix} w^k \\ 0 \end{bmatrix}$ , then  $X^T X u = \begin{bmatrix} 1 \\ 0 \end{bmatrix}$

$u = \begin{bmatrix} 0 \\ w^k \end{bmatrix}$ , then  $X^T X u = \begin{bmatrix} 0 \\ 1 \end{bmatrix}$

# Single-qubit cont.

## Lemma (Ide-k-case)

• weather condition suffices...

Let  $u$  be a 2-dimensional ~~unit~~ vector in  $\mathbb{D}[w]$  with  $\text{Ide}(u) > 0$ . Then there exists a sequence of H & T gates  $U_1 \dots U_k$  s.t.  $\text{Ide}(U_1 \dots U_k u) < \text{Ide}(u)$ .

### (reducibility)

For this proof it will be helpful to define **reducibility (mod  $\sqrt{2}$ )**. We say a vector  $v$  in  $\mathbb{Z}[w]^2$  is **reducible** if  $v = \sqrt{2} v'$ ,  $v'$  in  $\mathbb{Z}[w]^2$ .

Given  $v = \sqrt{2}^{\text{Ide}(u)} u$ , our goal is to find  $U_1 \dots U_k$  s.t.  $U_1 \dots U_k v$  is reducible, since

$$U_1 \dots U_k v = \sqrt{2} v' = \sqrt{2}^{\text{Ide}(u)} U_1 \dots U_k u$$

$\Downarrow$

$$v' = \sqrt{2}^{\text{Ide}(u)-1} U_1 \dots U_k u$$

Ex.

Let  $u = \frac{1}{\sqrt{2}} \begin{bmatrix} 1 \\ 1 \end{bmatrix}$  and let  $v = \sqrt{2} u = \begin{bmatrix} 1 \\ 1 \end{bmatrix}$ . Then

$$Hv = \frac{1}{\sqrt{2}} \begin{bmatrix} 1+1 \\ 1-1 \end{bmatrix} = \frac{1}{\sqrt{2}} \begin{bmatrix} 2 \\ 0 \end{bmatrix} = \sqrt{2} \begin{bmatrix} 1 \\ 0 \end{bmatrix}$$

# Proof of Ide-k-case

Let  $v = \sqrt{2}^{w^k(u)} u$ , so  $v$  has entries in  $\mathbb{Z}[w]$ .

We know  $\|v\| = \|\sqrt{2}^{w^k(u)} u\| = 2^m$  for  $m > 0$ .

Now if  $v = [v_1, v_2]^T$ , then we know:

① The  $\sqrt{2}$ -parts of  $|v_1|^2$  &  $|v_2|^2$  cancel

②  $|v_1|^2 \equiv |v_2|^2 \pmod{2}$

Notice that if  $v_1 \equiv v_2 \pmod{2}$ , then  $v_1 \pm v_2 \equiv 0 \pmod{2}$ , hence  $Hv$  is reducible:

$$Hv = \frac{1}{\sqrt{2}} \begin{bmatrix} v_1 + v_2 \\ v_1 - v_2 \end{bmatrix} = \frac{1}{\sqrt{2}} \begin{bmatrix} 2v_1 \\ 0 \end{bmatrix} = \sqrt{2} v'$$

Our immediate goal is to "make"  $v_1 \equiv v_2 \pmod{2}$ .

Let  $|v_1|^2 = a + b\sqrt{2}$ ,  $|v_2|^2 = c + d\sqrt{2}$ ,  $a, b, c, d \in \mathbb{Z}$ . By ② above,

③  $a \equiv b \pmod{2}$

④  $c \equiv d \pmod{2}$

## Case 1: $a \equiv b \equiv c \equiv d \equiv 0 \pmod{2}$

Let  $v_i = a_i w^3 + a_2 w^2 + a_3 w + a_4$ , so  $a = a_1^2 + a_2^2 + a_3^2 + a_4^2$   
 $b = a_1 a_2 + a_2 a_3 + a_3 a_4 - a_1 a_4$

That  $a \equiv b \equiv 0 \pmod{2}$  tells us that either

① all of  $a_i$  are even, so  $v_i$  is reducible

② all of  $a_i$  are odd, so

$$\begin{aligned} \sqrt{2} v_i &= (w - w^3) v_i = (a_2 - a_4) w^3 + (a_3 + a_1) w^2 + (a_4 + a_3) w + (a_3 - a_1) \\ &\equiv 2 v_i' \end{aligned}$$

$\therefore v_i = \sqrt{2} v_i'$ , i.e.  $v_i$  is reducible

③ only non-adjacent  $a_i, a_j$  are odd. Then

$v_i \equiv w^k(1+i) \pmod{2}$  for some  $k$ .

Since  $w^k(1+i) = w^k \sqrt{2}$ ,  $v_i = \sqrt{2}(w^k + \sqrt{2} v_i')$ , hence  $v_i$  is reducible

So, both  $v_1$  &  $v_2$  are reducible and the case is finished

# Ide - h-case cont.

Case 2:  $a \equiv c \equiv 0 \pmod{2}$ ,  $b \equiv d \equiv 1 \pmod{2}$ .

Letting  $v_1 = a_1 w^3 + a_2 w^2 + a_3 w + a_4$  again, we know evenly many  $a_i$  are odd.  $a_1 a_2 + a_2 a_3 + a_3 a_4 - a_4 a_1 \equiv 1 \pmod{2}$  tells us that

$$v_1 \equiv w^k(1+w) \pmod{2} \text{ for some } k$$

By the same argument,  $v_2 \equiv w^j(1+w) \pmod{2}$ . Now

$$w^{k-j} v_2 \equiv v_1 \pmod{2}.$$

Since  $T^{k-j} \begin{bmatrix} v_1 \\ v_2 \end{bmatrix} = \begin{bmatrix} v_1 \\ w^{k-j} v_2 \end{bmatrix}$ ,  $T^{k-j} v$  is reducible.

Case 3:  $a \equiv b \equiv c \equiv d \equiv 1 \pmod{2}$

Letting  $v_1 = a_1 w^3 + a_2 w^2 + a_3 w + a_4$ , we see that an odd number of  $a_i$ 's are odd. Any such choice gives

$$b = a_1 a_2 + a_2 a_3 + a_3 a_4 - a_4 a_1 \equiv 0 \pmod{2}, \text{ so this case is impossible.}$$

# LDE-k-case cont.

Case 3:  $a \equiv c \equiv 1 \pmod{2}$ ,  $b \equiv d \equiv 0 \pmod{2}$

Letting  $V_1 = a_1 w^3 + a_2 w^2 + a_3 w + a_4$ ,  $a \equiv 1 \pmod{2}$  &  $b \equiv 0 \pmod{2}$  imply that an odd number of  $a_i$ 's are odd, so

$$V_1 \equiv w^k \text{ or } w^k(1+w+i) \pmod{2}$$

$$V_2 \equiv w^j \text{ or } w^j(1+w+i) \pmod{2}$$

If  $V_1 \equiv w^k \pmod{2}$  &  $V_2 \equiv w^j \pmod{2}$  or

$$V_1 \equiv w^k(1+w+i) \pmod{2} \text{ & } V_2 \equiv w^j(1+w+i) \pmod{2},$$

then  $V_1 \equiv w^{k-j} V_2 \pmod{2}$ , so  $T^{k-j} V$  is reducible

Otherwise, WLOG  $V_1 \equiv w^k \pmod{2}$  &  $V_2 \equiv w^j(1+w+i) \pmod{2}$ .

Let  $V_1' = V_1 + w^{k-i+1} V_2$ ,  $V_2' = V_1 - w^{k-i+1} V_2$ , so

$$\begin{aligned} V_1' &\equiv w^k + w^{k+1}(1+w+i) \pmod{2} \\ &\equiv w^k(1+w+i+w) \pmod{2} \\ &\equiv 1+w+i+w^3 \pmod{2} \end{aligned}$$

$$\begin{aligned} V_2' &\equiv w^k + w^{k+1}(1+w+i) \pmod{2} \\ &\equiv w^k(1+w+i+w^3) \pmod{2} \\ &\equiv 1+w+i+w^3 \pmod{2} \end{aligned}$$

By the first case,  $V_1'$  &  $V_2'$  are both reducible, and if  $V_1' = \sqrt{2} V_1''$ , then

$$\begin{aligned} 1+w+i+w^3 &\equiv (v-w)(aw^3+bw^2+cw+d) \pmod{2} \\ &\equiv (b-d)w^3 + (c+a)w^2 + (d+b)w + (c-a) \pmod{2} \end{aligned}$$

So  $b+d \equiv c+a \equiv 1 \pmod{2}$ , hence  $V_1'' \equiv w^n(1+w) \pmod{2}$

$$V_2'' \equiv w^n(1+w) \pmod{2}$$

So  $V_1'' \equiv w^{n-n} V_2'' \pmod{2}$ , and

$$\begin{aligned} T^{n-n} H T^{k-i+1} \begin{bmatrix} V_1 \\ V_2 \end{bmatrix} &= T^{n-n} \frac{1}{\sqrt{2}} \begin{bmatrix} V_1' \\ V_2' \end{bmatrix} = T^{n-n} \begin{bmatrix} V_1'' \\ V_2'' \end{bmatrix} \\ &= \begin{bmatrix} V_1'' \\ w^{n-n} V_2'' \end{bmatrix} \end{aligned}$$

$\therefore T^{n-n} H T^{k-i+1} V$  is reducible



# Exact synthesis

The hard part is over now :)

Let's start with single qubit case

## Thm

Let  $u$  be a unit vector in  $\mathbb{D}[w]^2$ . Then  $\exists u_1 \dots u_n$  from  $\{H, T\}$  such that  $u_1 \dots u_n u = \begin{bmatrix} 1 \\ 0 \end{bmatrix}$

## Pf

By induction on  $\text{Ide}(u)$  using the previous two lemmas.

For the  $n$ -qubit case, we extend this to a column lemma using two-level operators of the form  $H_{ij}$  &  $T_{ij}$

## Thm (column lemma)

Let  $u$  be a unit vector in  $\mathbb{D}[w]^d$ . Then there exists a sequence  $u_1 \dots u_n$  of two-level,  $d \times d$  gates  $H_{ij}$  &  $T_{ij}$  such that  $u_1 \dots u_n u = \begin{bmatrix} 1 \\ 0 \\ \vdots \\ 0 \end{bmatrix}$

## Pf

By induction on  $\text{Ide}(u)$ . If  $\text{Ide}(u) = 0$ , then  $u = w^* e_i$ , so  $x_{a,i} T_{a,i}^k = H_{b,i} T_{a,i}^4 H_{a,i} T_{a,i}^k$  suffices.

If  $\text{Ide}(u) = k \geq 1$ , let  $v = \sqrt{2}^k u$ . Then

$$2^k = \|v\|^2 = |v_1|^2 + \dots + |v_d|^2$$

$$\Rightarrow |v_1|^2 + \dots + |v_d|^2 \equiv 0 \pmod{2}$$

Since for any  $|v_i|^2 = a + b\sqrt{2}$ , we can't have  $a \equiv b \equiv 1 \pmod{2}$ , there exist  $i, j$  s.t.  $|v_i|^2 \equiv |v_j|^2 \pmod{2}$ . Use the  $\text{Ide}-k$  lemma with  $u = \begin{bmatrix} u_i \\ u_j \end{bmatrix}$  and repeat in pairs until every  $v_i$  is reducible.

# Finishing the job

Recall that all we need to synthesize over two-level operators is a column lemma, so here's where we are

Unitaries over  $\mathbb{D}[w]$

↓ Done!

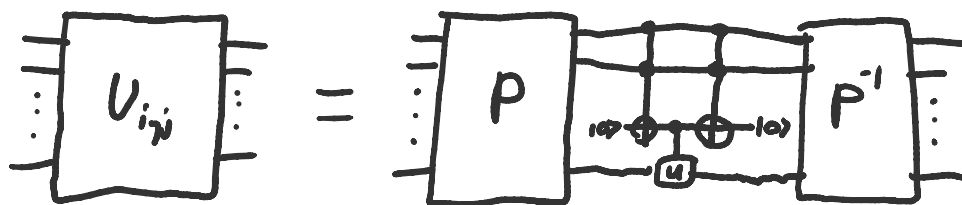
Two-level  $\{H, T\}$  matrices

↓ still need to do

Clifford + T

## (Decomposition of two-level $H$ & $T$ )

Recall that any 2-level unitary can be decomposed as a controlled gate and Toffoli's, i.e.

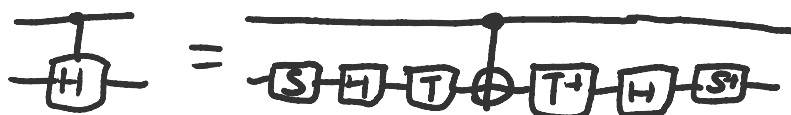


Since the Toffoli has a known Clifford + T implementation, we only need controlled- $H$  and  $T$ .

For controlled- $T$ , note that  $T$  is actually controlled- $wI$ , so



For controlled- $H$ ,  $H = S^{\dagger} H T^{\dagger} X T H S$  ← why? so



# Complexity analysis

It turns out that this is **optimal for single-qubit unitaries**  
For  $n$ -qubit unitaries however it produces very inefficient circuits.

## Prop.

For an  $n$ -qubit matrix  $U$  over  $\mathbb{D}[w]$ , The  
Giles-Selinger algorithm produces circuits of  
size  $O(3^{2^n} k)$  where  $k = \text{Ide}(U)$

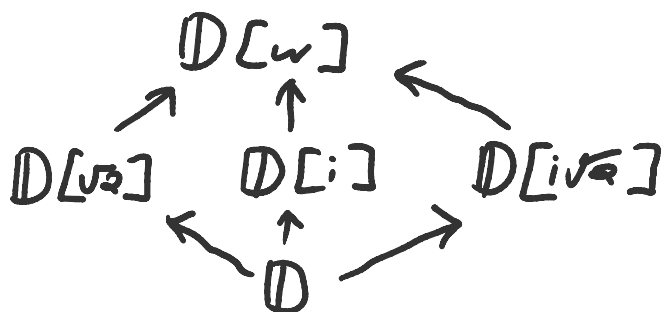
## Note

Kliuchnikov 2013 - Synthesis of unitaries with Clifford+T  
(arXiv:1306.3200)

reduced this to  $O(4^n n k)$  using **reflection** operators.  
(this is a really cool method :))

## Other correspondences

Number-theoretic characterization has  
led to some other interesting correspondences.  
In particular, the (partial) lattice of sub-rings



Of  $\mathbb{D}[w]$  has similarly been characterized:

- $\mathbb{D}[\sqrt{2}] \equiv \{X, CX, CCX, H, CH\}$
- $\mathbb{D} \equiv \{X, CX, CCX, H \oplus H\}$
- $\mathbb{D}[i] \equiv \{X, CX, CCX, wH, S\}$
- $\mathbb{D}[i\sqrt{2}] \equiv \{X, CX, CCX, F \propto \sqrt{H}\}$

# Ring round-off & Grid-synth

Since a circuit over Clifford+T corresponds **precisely** to a unitary over  $\mathbb{D}[w]$ , we can approximate  $U$  by finding  $V$  with entries in  $\mathbb{D}[w]$  such that

$$\|U - V\| \leq \epsilon$$

This is called the **round-off problem**. Ross & Selinger (2014, arXiv:1403.2975) famously gave an **effectively optimal** algorithm for rounding off  $R_z(\theta)$  rotations into  $\mathbb{D}[w]$ , called **Grid-synth**.

## Thm (Ross-Selinger) (over Clifford+T)

An  $R_z(\theta)$  gate can be approximated **optimally** via  $\mathbb{D}[w]$  and exact synthesis given a factoring oracle, or with typical length  $3\log_2(1/\epsilon) + O(\log(\log(1/\epsilon)))$ .

## Pf

Very far outside our scope...

The idea is to find some  $u \in \mathbb{D}[w]$  in an  $\epsilon$ -region of the unit circle with **minimal**  $|d_e|$



This is shown to be poly-time enumerable.

Then, try to find  $t \in \mathbb{D}[w]$  s.t.  $\begin{bmatrix} u \\ t \end{bmatrix}$  is a unit vector.  
**requires solving a diophantine equation  $|t|^2 = 1 - |u|^2$**